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 Did you mean: ***inserted phase error into byte stream***

Method and circuit for built in self test of **phase** locked loops

US Pat. 6396889 - Filed Nov 2, 1998 - LogicVision, Inc.

 When the stimulus **bit stream** 184 is alternating 1's and 0's, Y1 will be 0% of the ... 12, the delay T1 **inserted into** the PLL input signal is constant, ...

Synchronizing system for spread spectrum transmissions between small earth ...

US Pat. 4617674 - Filed Jul 14, 1993 - RCA Corporation

 ... to either acquire or maintain synchronization must be **inserted** by the MES **into the bit stream** ... If the **phase error** still exceeds predetermined limits, ...

Skew compensating interface for operation with arbitrary data

US Pat. 6704890 - Filed Jan 29, 2001 - Nortel Networks Limited

 39 40 25 arranging a source of digital data **into** n parallel **bit-** streams; ... reference bitstream to indicate to the transmit side any detected **phase error**. ...

Write format for digital data storage

US Pat. 6167550 - Filed Aug 14, 1998 - Overland Data, Inc.

 8 is a flowchart that illustrates one embodiment of a process utilized to insert bits **into** data **stream** regions having low **phase** information content. FIG. ...

Magnetic tape drive apparatus including a variable rate encoder

US Pat. 6597526 - Filed Aug 14, 1998 - Overland Storage, Inc.

 In both cases, the calculated **phase error** is typically converted **into** an analog ... comprise a write head for writing the data **stream** including the **inserted** ...

High speed digital to analog converter

US Pat. 6781537 - Filed Jun 10, 2003 - Nortel Networks Limited

 Alternatively, known **phase** shifters can be **inserted into** each ... to be carrying a serial **bit stream** having a **bit** rate equivalent to the line rate, that is, ...

Synchronous multiport digital 2-way communications network using T1 PCM on a ...

US Pat. 5124980 - Filed Mar 20, 1989

 The output serial **bit stream** is clocked **into** an 8-**bit** serial-to-parallel conversion ... or decrement its frame time, thereby correcting the **phase error**. ...

Transmission system with adaptive interleaving

US Pat. 4901319 - Filed Mar 16, 1988 - General Electric Company

 Basically, as the **phase error** increases, the decorrelation time decreases for a fixed **phase** ... sync signal is **inserted into** the interleaved data **stream**. ...

System for cross-connecting high speed digital SONET signals

US Pat. 4967405 - Filed Dec 9, 1998 - Transwitch Corporation

 Five stuff control bits are provided at column, **bit** positions C4-B5, ... bytes are **inserted** as required **into** the data **stream** via multiplexer 1240 as ...



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inserted phase error into bit stream

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